

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7	mounting near7 silicon adj2 resistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:39
L2	2	assembling near7 silicon adj2 resistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:40
L3	33	select\$3 adj7 (shape near3 capacitor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:40
L4	7	custom\$6 adj7 (shape\$1 near3 capacitor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:40
L5	41	silicon adj3 carrier same passive	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:40
L6	10	physical adj design near7 passive	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:41
L7	3	physical adj design near7 decoupling	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:41
L8	3	physical adj design near7 carrier	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:41

L9	412	design\$4 near7 decoupling near3 (capacitor\$1 or rsistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:41
L10	58439	design\$4 near7 (package or carrier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:41
L11	37	L9 and L10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:41
L12	43	(sizes or sizing) near7 decoupling near3 (capacitor\$1 or rsistor\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/20 15:42

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